

**IN THE SPECIFICATION:**

Please add the following paragraph before the paragraph beginning on page 1, after line 5:

--This is a divisional application of U.S. patent application Serial No. 09/756,068, filed on January 8, 2001 which is a divisional application of U.S. Patent 6,230,258 filed on August 31, 1998.--

Please amend the paragraph on page 39, lines 14-22 with the following:

The instruction execution halt means 104 receives the operation flag state signal 111 output from the operation flag hold means 101 and the conditional instruction designation signal 113 output from the instruction decoding means 102. When a condition is not satisfied, the instruction execution halt means 104 outputs the instruction execution halt signal ~~113~~114 to the instruction execution means 103 so that the instruction execution means 103 halts its instruction execution.

Please amend the paragraph on page 43, lines 19-25 with the following:

Figs. 6A and 6B show intermediate code sequences including conditional transfer instructions. These intermediate code sequences are respectively generated by the present compiler from the intermediate code sequences not including conditional transfer instructions shown in Figs. 5A and 5B in steps S302 - ~~S118~~S318 of the flowchart shown in Fig. 3.